# OBJECTIVE:

* Understand and use the ADC of AVR.
* Understand how to use the ADC for measurement.

# References:

* Experiment Experiment guide, chapters 11
* AN2538-ADC-of-megaAVR-in-SingleEnded-Mode-00002538A.pdf
* AVR120: Characterization and Calibration of the ADC on an AVR

# EXPERIMENT 1: MEASURING SINGLE-ENDED SIGNALS

1. Connect the ADC\_VR1 and ADC\_VR2 signals from header J86 to the ADC0 and ADC1 inputs. Connect UART0 to the RS232 module and connect the USB-Serial cable to the computer. Connect ADC\_VR1 and ADC\_VR2 to the test points on header J56. Make sure not to connect them to the GND pins on the header. Write a program to perform the following tasks:
2. Select the VREF voltage as the internal VCCA voltage. Initialize UART with self-selected configuration. (Note: configure the Hercules software on the computer in a similar way). Right-click on the Hercules screen to select HEX Enable.
3. Write a program to sample the signal input to ADC0 and send it to the computer using UART0 with the following transmission frame every 1 second. The 1-second delay can be achieved using either a delay function or a timer.  
   0x55 ADCH ADCL 0xFF
4. Change the voltage applied to ADC0, measure it using a VOM, and compare it with the sampled ADC result. Fill in the table in the report.
5. Connect an LCD to one of the AVR ports. Add to the existing program the functionality to calculate the input voltage and display it on the LCD.
6. Change the reference voltage to the internal 2.56V reference. Repeat steps c, d, and e, assuming the reference voltage is accurately set to 2.56V.
7. Measure the voltage on the VREF pin (header J57) using a VOM.

# EXPERIMENT 2: MEASURING OFFSET ERROR AND GAIN ERROR

1. Calculate the offset error and gain error of the ADC.
2. Rewrite the program with the requirements mentioned in question e of exercise 1, using the calibrated ADC. Vref = VCCA. Send the calibrated ADC values to the computer and display the measured voltage values on the LCD.  
   (Note: Please refer to the documentation "AVR120: Characterization and Calibration of the ADC on an AVR" for detailed instructions and guidelines on the calibration process.

# EXPERIMENT 3: MEASURING ADC IN BIAS MODE

1. Calibrate channel VR1 to a voltage level of 2.5V and connect it to ADC0.
2. Write a program to initialize the ADC in bias mode with two input channels, ADC0 and ADC1, amplification factor of 10, and a reference voltage of 2.56V. Start the ADC in FreeRunning mode.
3. Write a program to display the voltage value of VR1 on the LCD and send the ADC measurement results to the computer every 1 second, as described in experiment 1.

# EXPERIMENT 4:MEASURING OFFSET ERROR AND GAIN ERROR IN BIAS MODE

1. Connect both ADC1 and ADC0 to the voltage ADC\_VR1. Adjust this voltage to 1V and record the measured ADC value. This value represents the offset error.
2. From the offset error value, calculate the gain error using Table 4.
3. Rewrite the program from question c in exercise 3, using the calibrated ADC value.

# EXPERIMENT 5: MEASURING TEMPERATURE USING MCP9701

1. Connect the sensor to header J73.
2. Connect the voltage signal V\_TEMP on header J18 to ADC0.
3. Write a program to measure the voltage value of V\_TEMP with the calibration parameters as in experiment 1, calculate the temperature value, and display it on the LCD.

# EXPERIMENT 1:

1. Answering the questions:
2. In Single Conversion mode, how do you start a sampling cycle and check when it completes?
3. How do you select an ADC channel?
4. Does Atmega324 have an internal temperature measurement channel?
5. What is the formula to calculate the input voltage from the ADC when VREF=VCCA?
6. What is the formula to calculate the input voltage from the ADC when VREF=2.56V?
7. Record the ADC value compared to the measurement result from the measuring instrument. Calculate the voltage value based on the ADC value with VREF=VCCA.

| VADC0 (V) | 0 | 0.5 | 1 | 1.5 | 2 | 2.5 | 3 | 3.5 | 4 | 4.5 | 5 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL |  |  |  |  |  |  |  |  |  |  |  |
| ADCH-ADCL (Theoretical) |  |  |  |  |  |  |  |  |  |  |  |
| Error (LSB) |  |  |  |  |  |  |  |  |  |  |  |

*Bảng 1*

1. Repeat step f with VREF=2.56V. Compare the calculation formula and the error with step e.

| VADC0 (V) | 0 | 0.5 | 1 | 1.5 | 2 | 2.5 | 3 | 3.5 | 4 | 4.5 | 5 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL |  |  |  |  |  |  |  |  |  |  |  |
| ADCH-ADCL (Theoretical) |  |  |  |  |  |  |  |  |  |  |  |
| Error (LSB) |  |  |  |  |  |  |  |  |  |  |  |

*Bảng 2*

1. Repeat step g with VREF being the value measured from a VOM.

VREF = ...................................................................

| VADC0 (V) | 0 | 0.5 | 1 | 1.5 | 2 | 2.5 | 3 | 3.5 | 4 | 4.5 | 5 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL |  |  |  |  |  |  |  |  |  |  |  |
| ADCH-ADCL (Theoretical) |  |  |  |  |  |  |  |  |  |  |  |
| Error (LSB) |  |  |  |  |  |  |  |  |  |  |  |

*Bảng 3*

1. Program source code with comments

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# EXPERIMENT 2:

1. Answering the questions:
   1. What are the types of errors for an ADC?
   2. What is offset error?
   3. What is gain error?
   4. What are the units of measurement for offset error and gain error?
   5. Using the results from Table 1, taking two measurement points at Vin = 1 V and Vin = 4.5V, calculate the offset error and gain error.
2. Measurement and Calculation:
3. The measured ADC values corresponding to the input channels.

| VADC0 (V) | 0 | 0.5 | 1 | 1.5 | 2 | 2.5 | 3 | 3.5 | 4 | 4.5 | 5 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL (calibrated) |  |  |  |  |  |  |  |  |  |  |  |
| ADCH-ADCL (Theoretical) |  |  |  |  |  |  |  |  |  |  |  |
| Error (LSB) |  |  |  |  |  |  |  |  |  |  |  |

*Bảng 4*

1. Program source code with comments

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# EXPERIMENT 3:

1. Answering the questions:
   1. In differential mode, what is the voltage range within which the input voltages of the ADC oscillate?
   2. When selecting the signal pair ADC0, ADC1 with a gain of 10, VREF=2.56V, and VADC0 = 2.5V, what is the measurement range for ADC1?
   3. Fill in the measured values and calculate the input voltage based on the theoretical information.

| VADC1 – VADC0 (mV) | 0 | 50 | 100 | 150 | 200 | 250 | -50 | -100 | -150 | -200 | -250 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL |  |  |  |  |  |  |  |  |  |  |  |
| Values |  |  |  |  |  |  |  |  |  |  |  |

*Bảng 5*

1. Program source code with comments

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# EXPERIMENT 4:

1. Answering the questions:
   1. How to measure offset error in differential mode?
   2. Results obtained after calibration.chỉnh

| VADC1 – VADC0 (mV) | 0 | 50 | 100 | 150 | 200 | 250 | -50 | -100 | -150 | -200 | -250 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ADCH-ADCL |  |  |  |  |  |  |  |  |  |  |  |
| Values |  |  |  |  |  |  |  |  |  |  |  |

1. Program source code with comments

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# EXPERIMENT 5:

1. Answering the questions:
2. According to the datasheet, what is the thermal coefficient of the MCP9701 sensor ?
3. If VREF = VCCA is used, what is the measurement range of the circuit? What is the resolution of the measurement circuit in degrees Celsius ?
4. If VREF = 2.56V is used, what is the measurement range of the circuit? What is the resolution of the measurement circuit in degrees Celsius ?
5. If we use the ADC in differential mode, input a voltage of 400 + 19.53\*20 (mV) to ADC0, and connect the output of MCP9751 to ADC1, with a gain of 10 and VREF=2.56V, what is the measurement range and resolution of the measurement circuit ?
6. Program source code with comments

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